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APPLICATION NO.	FIL	ING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/647,939	10/647,939 08/26/2003		John L. Galvagni	AVX-187-DIV	3080
22827	7590	09/14/2005		EXAMINER	
DORITY &		•	PHAN, THIEM D		
POST OFFICE BOX 1449 GREENVILLE, SC 29602-1449				ART UNIT	PAPER NUMBER
,				3729	_

DATE MAILED: 09/14/2005

Please find below and/or attached an Office communication concerning this application or proceeding.

	Application No.	Applicant(s)
	10/647,939	GALVAGNI ET AL.
Office Action Summary	Examiner	Art Unit
	Tim Phan	3729
The MAILING DATE of this communication app Period for Reply	pears on the cover sheet	vith the correspondence address
A SHORTENED STATUTORY PERIOD FOR REPL' WHICHEVER IS LONGER, FROM THE MAILING D.  Extensions of time may be available under the provisions of 37 CFR 1.1 after SIX (6) MONTHS from the mailing date of this communication.  If NO period for reply is specified above, the maximum statutory period of Failure to reply within the set or extended period for reply will, by statute Any reply received by the Office later than three months after the mailing earned patent term adjustment. See 37 CFR 1.704(b).	ATE OF THIS COMMUN 36(a). In no event, however, may a will apply and will expire SIX (6) MC 1, cause the application to become a	ICATION: reply be timely filed  NTHS from the mailing date of this communication.  BANDONED (35 U.S.C. § 133).
Status		
1) Responsive to communication(s) filed on 27 A	ugust 2003.	
•	action is non-final.	
3) Since this application is in condition for allowa closed in accordance with the practice under E		
Disposition of Claims		
4) Claim(s) 6-13 is/are pending in the application 4a) Of the above claim(s) is/are withdray 5) Claim(s) is/are allowed. 6) Claim(s) 6-13 is/are rejected. 7) Claim(s) is/are objected to. 8) Claim(s) are subject to restriction and/or	wn from consideration.	
Application Papers		
9) The specification is objected to by the Examine 10) The drawing(s) filed on is/are: a) accomplicated any not request that any objection to the Replacement drawing sheet(s) including the correct to by the Examine	epted or b) objected t drawing(s) be held in abey tion is required if the drawir	ance. See 37 CFR 1.85(a). g(s) is objected to. See 37 CFR 1.121(d).
Priority under 35 U.S.C. § 119		
12) Acknowledgment is made of a claim for foreign a) All b) Some * c) None of:  1. Certified copies of the priority document 2. Certified copies of the priority document 3. Copies of the certified copies of the priority application from the International Burea * See the attached detailed Office action for a list	ts have been received. ts have been received in city documents have bee u (PCT Rule 17.2(a)).	Application No n received in this National Stage
Attachment(s)		
1) Notice of References Cited (PTO-892)		y Summary (PTO-413)
<ul> <li>2) Notice of Draftsperson's Patent Drawing Review (PTO-948)</li> <li>3) Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08)</li> <li>Paper No(s)/Mail Date 8/27/03 &amp; 11/6/03.</li> </ul>	()	o(s)/Mail Date Informal Patent Application (PTO-152) 

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## **DETAILED ACTION**

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## Title

1. The following title is suggested: "Method for Adjusting the ESR of multilayer component".

#### Abstract

2. Applicants are reminded of the proper language and format for an abstract of the disclosure.

The abstract should be in narrative form and generally limited to a single paragraph on a separate sheet within the range of 50 to 150 words. It is important that the abstract not exceed 150 words in length since the space provided for the abstract on the computer tape used by the printer is limited. The form and legal phraseology often used in patent claims, such as "means" and "said," should be avoided. The abstract should describe the disclosure sufficiently to assist readers in deciding whether there is a need for consulting the full patent text for details.

The language should be clear and concise and should not repeat information given in the title. It should avoid using phrases which can be implied, such as, "The disclosure concerns," "The disclosure defined by this invention," "The disclosure describes," etc.

Currently, the Abstract has 174 words counted. Appropriate correction is required.

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# Claim Rejections - 35 USC § 102

3. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless -

(e) the invention was described in (1) an application for patent, published under section 122(b), by another filed in the United States before the invention by the applicant for patent or (2) a patent granted on an application for patent by another filed in the United States before the invention by the applicant for patent, except that an international application filed under the treaty defined in section 351(a) shall have the effects for purposes of this subsection of an application filed in the United States only if the international application designated the United States and was published under Article 21(2) of such treaty in the English language.

4. Claims 6-7 and 9-13 are rejected under 35 U.S.C. 102(e) as being anticipated by Novak (US 6,215,372).

As applied to claim 6, Novak teaches a method for reducing electrical resonances and noise propagation in multilayer board, comprising:

- producing a multilayer component (Fig. 6, 600) including at least first and second electrode layers (Fig. 6, items 602, 604, 606 or 608) separated by an insulating layer (Fig. 6, items 610, 612 or 614);
- providing a resistive layer (Fig. 6, 650 or 652) with the insulating layer and the first and second electrically conductive layers; and
- adjusting the ESR (Col. 8, lines 59-61; col. 9, lines 5-20) of the component by varying the effective resistance of the resistive layer.

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As applied to claim 7, Novak teaches that the providing step comprises:

• providing the resistive layer (Fig. 6, 650 or 652) between the insulating layer (Fig. 6, 610 or 614) and one of the first or second electrically conductive layers (Fig. 6, 602 or 608).

As applied to claims 9 and 11, Novak teaches that the adjusting step comprises:

• varying the effective resistance of the resistive layer by adjusting the thickness of the resistive layer (Fig. 1, h) for the capacitance value equation (Col. 10, line 50).

As applied to claims 10 and 12, Novak teaches that the adjusting step comprises:

• varying the effective resistance of the resistive layer by adjusting the composition of the resistive layer (Col. 9, lines 5-20).

As applied to claim 13, Novak teaches a method for reducing electrical resonances and noise propagation in multilayer board:

- producing a multilayer component (Fig. 6, 600) having a plurality of successively stacked electrode layers (Fig. 6, items 602, 604, 606 or 608);
- providing separate insulating layers (Fig. 6, items 610, 606 or 614) sandwiched between each of the electrode layer; and
- varying a physical property of selected of the separate insulating layers with different capacitance (Fig. 6, 652) whereby the resonance characteristics of the multi-layer component are adjusted (Abstract).

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# Claim Rejections - 35 USC § 103

- 5. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:
  - (a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negatived by the manner in which the invention was made.
- 6. Claim 8 is rejected under 35 U.S.C. 103(a) as being unpatentable over Novak.

Novak teaches a method for reducing electrical resonances and noise propagation in multilayer board, including the adjusting steps of:

- perforating one of the first or second electrically conductive layers (Fig. 10, items 1002, 1004, 1006 or 1008) with a plurality of through-holes (Fig. 10, 1022 or 1024); and
- varying the effective resistance of the resistive layer by adjusting capacitive islands (Fig. 6, 652 or Fig. 10, 1052) at selected areas or distances from vias (Fig. 6, 622 or Fig. 10,10220) whereby the extent of coverage of the perforated electrode varies the effective resistance of the resistive layer, except for detailing these selected areas or distances as varying and spacing diameters of through-holes.

It would have been obvious to one of ordinary skill in the art at the time the invention was made to consider these selected areas or distances as varying and spacing diameters of through-holes, which can vary and match the impedance of the multilayer circuit in order to

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reduce noise and any ground bounce signal.

## Conclusion

The prior art made of record and not relied upon is considered pertinent to applicant's 7. disclosure.

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Tim Phan whose telephone number is 571-272-4568. The examiner can normally be reached on M - F, 9AM - 5PM.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor. Peter Vo can be reached on 571-272-4690. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

Tim Phan Examiner Art Unit 3729

September 11, 2005